Comp E 475

Microprocessors

HW7 : ALU module

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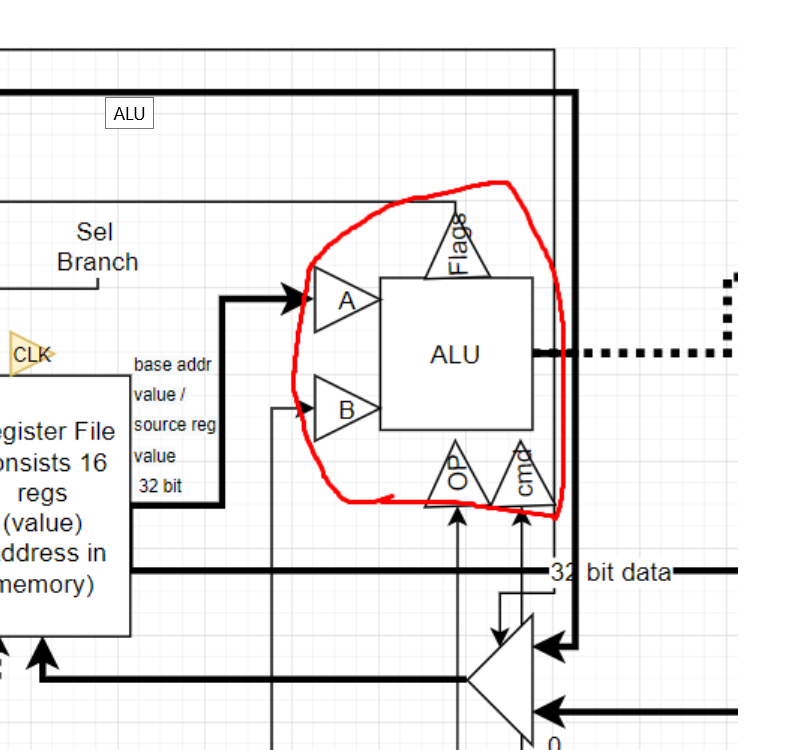
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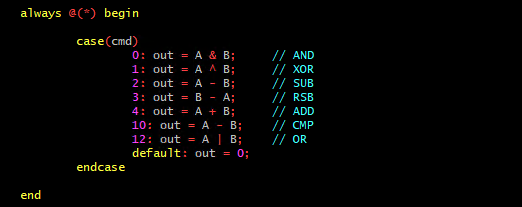
# Task Description

*In this assignment, we have to implement ALU part of controller in verilog acording to following:*

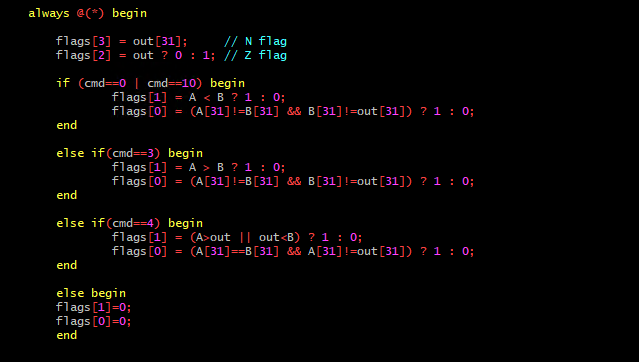
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# Solution

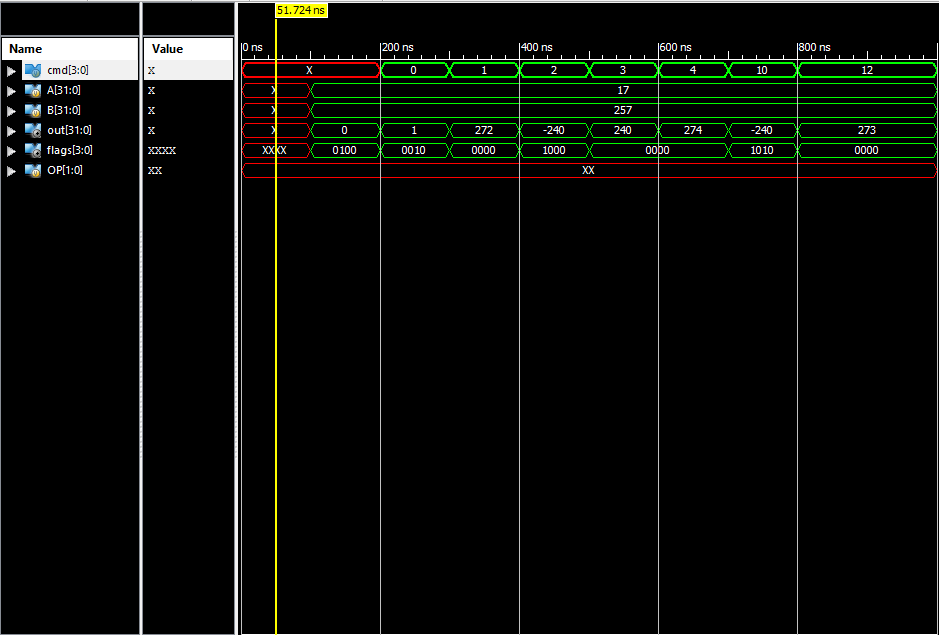
*For ALU output we have to use combination of following inputs: “A”, “B” and “cmd”. To define output we can use multiplexer where cmd is selection signal.*

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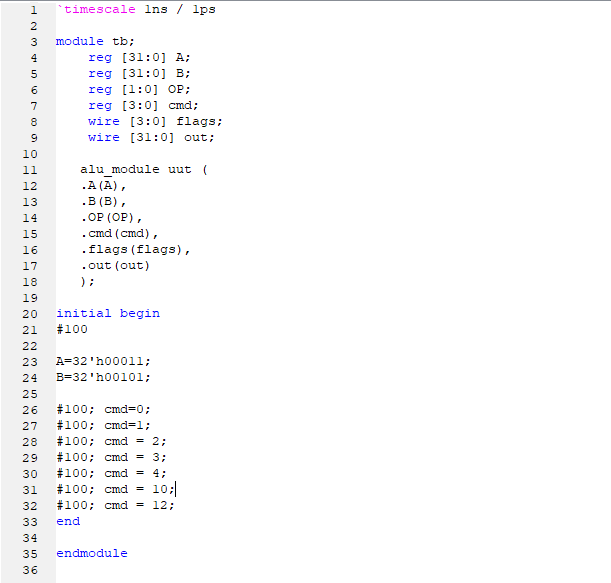
For flags:



# Simulation & Verification



TestBench:



# Conclusion

In this assignment, I learned how ALU is making decisions based on inputs